

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Adrianus Josephus Bink et al.

Group Art Unit: 2185

Serial No.: 10/570,290

Examiner: Giardino, Jr., Mark A.

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For: INTERGRATED CIRCUIT AND A METHOD OF CACHE  
REMAPPING

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION UNDER 37 C.F.R. 1.114

Sir:

In response to the Final Office Action mailed March 13, 2008, please find the following:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.

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CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

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Typed Name: Mark A. Wilson